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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Technology Center 2100

Applicant(s): Tremblay, Marc; Joy, William
Assignee: Sun Microsystems, Inc.
Title: Local And Global Register Partitioning In VLIW Processor
Serial No.: 09/204,585 Filing Date: December 3, 1998
Examiner: D. Eng Group Art Unit: 2155
Docket No.: SP-3288 US

San Jose, California
September 14, 2001

Attn: Official Draftsperson
COMMISSIONER FOR PATENTS
Washington, D. C. 20231

SUBMISSION OF FORMAL DRAWINGS

Dear Sir:

Applicant(s) submit(s) eleven (11) sheets of formal drawings, consisting of Figures 1, 2, 3, 4, 5A, 5B, 5C, 6, 7, 8A, 8B, 9, and 10, in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (949) 718-5200.

EXPRESS MAIL LABEL NO:

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Respectfully submitted,

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